

PALM INTRANET

Day : Monday
 Date: 1/1/2007
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Inventor Name Search Result

Your Search was:

Last Name = CORY

First Name = WARREN

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<u>10082490</u>	Not Issued	61	02/22/2002	Channel bonding control logic architecture	CORY, WARREN E.
<u>10090250</u>	Not Issued	93	03/01/2002	HIGH SPEED CONFIGURABLE TRANSCEIVER ARCHITECTURE	CORY, WARREN E.
<u>10090251</u>	7088767	150	03/01/2002	METHOD AND APPARATUS FOR OPERATING A TRANSCEIVER IN DIFFERENT DATA RATES	CORY, WARREN E.
<u>10090286</u>	6617877	150	03/01/2002	VARIABLE DATA WIDTH OPERATION IN MULTI-GIGABIT TRANSCEIVERS ON A PROGRAMMABLE LOGIC DEVICE	CORY, WARREN E.
<u>10090519</u>	7111220	150	03/01/2002	NETWORK PHYSICAL LAYER WITH EMBEDDED MULTI-STANDARD CRC GENERATOR	CORY, WARREN E.
<u>10234978</u>	7099426	150	09/03/2002	FLEXIBLE CHANNEL BONDING AND CLOCK CORRECTION OPERATIONS ON A MULTI-BLOCK DATA PATH	CORY, WARREN E.
<u>10618146</u>	6960933	150	07/11/2003	VARIABLE DATA WIDTH OPERATION IN MULTI-GIGABIT TRANSCEIVERS ON A PROGRAMMABLE LOGIC DEVICE	CORY, WARREN E.
<u>10622204</u>	Not Issued	30	07/18/2003	Distributed adaptive channel bonding control for improved tolerance of inter-channel skew	CORY, WARREN E.
<u>10648121</u>	6970013	150	08/25/2003	VARIABLE DATA WIDTH CONVERTER	CORY, WARREN E.
<u>10660449</u>	Not	30	09/11/2003	Variable latency buffer and	CORY, WARREN E.

	Issued			method of operation	
<u>10971220</u>	Not Issued	30	10/22/2004	Error checking parity and syndrome of a block of data with relocated parity bits	CORY, WARREN E.
<u>11048443</u>	Not Issued	30	02/01/2005	Method and system for pipelined decryption	CORY, WARREN E.
<u>60589869</u>	Not Issued	159	07/20/2004	Method and system for pipelined decryption	CORY, WARREN E.
<u>60589887</u>	Not Issued	159	07/20/2004	Error checking parity and syndrome of a block of data with relocated parity bits	CORY, WARREN E.

Inventor Search Completed: No Records to Display.

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